Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (03-08) Approved for use through 04/30/2008, OMB 0651-0031

Under the Paperwork Reduction Act of 19	•	U.S ond to a	S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE collection of information unless it contains a valid OMB control number.
 	Application Number		10766768
APR 30 2008 2	Filing Date		2004-01-27
NFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor	Rona	ld Duane McCallister
(Not for submission under 37 CFR 1.99)	Art Unit		
(Notice custimosism amost of of K 1.55)	Examiner Name	Baya	rd, Emmanuel
	Attorney Docket Numb	er	2298-010

	U.S.PATENTS								
Examin Initial*	ier Ci No		Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear		
/E.B	1		4085378		1978-04-18	Ryan, et al.			
200000000000000000000000000000000000000	2		5105445	А	1992-04-14	Karam, et al.			
000000000000000000000000000000000000000	3		5819165	А	1998-10-06	Hulkko, et al.			
200000000000000000000000000000000000000	4		5903823	Α	1999-05-11	Moriyama, et al.			
000000000000000000000000000000000000000	5		6081158	А	2000-06-27	Twitchell, et al.			
000000000000000000000000000000000000000	6		6112059	Α	2000-08-29	Schwent, et al.			
V	7		6125266	Α .	2000-09-26	Matero, et al.	·		
/E.B	./ 8		6133789	Α	2000-10-17	Braithwaite			

(Not for submission under 37 CFR 1.99)

				
Application Number		10766768		
Filing Date		2004-01-27		
First Named Inventor	Rona	ald Duane McCallister		
Art Unit				
Examiner Name	Baya	ard, Emmanuel		
Attorney Docket Number		2298-010		

/E.B./	9	6166601	A	2000-12-26	Shalom, et al.	
	10	6281936	B1	2001-08-28	Twitchell, et al.	
000000000000000000000000000000000000000	11	6285412	B1	2001-09-04	Twitchell	
	12	6298097	B1	2001-10-02	Shalom	
888900000000000000000000000000000000000	13	6335767	B1	2002-01-01	Twitchell, et al.	
200000000000000000000000000000000000000	14	6337599	B2	2002-01-08	Lee	
000000000000000000000000000000000000000	15	6507731	B1	2003-01-14	Hasegawa	
998999999999999999999999999999999999999	16	6515712	B1	2003-02-04	Jeong	
825000000000000000000000000000000000000	17	6600516	B1	2003-07-29	Danielsons, et al.	
V	18	6775330	B2	2004-08-10	Bach, et al.	
/E.B.	[/] 19	6794939	B2	2004-09-21	Kim, et al.	

(Not for submission under 37 CFR 1.99)

Application Number	-	10766768		
Filing Date		2004-01-27		
First Named Inventor Rona		ld Duane McCallister		
Art Unit				
Examiner Name	Baya	rd, Emmanuel		
Attorney Docket Number		2208 010		

/E.B.,	20	6819720	B1	2004-11-16	Willetts	
/E.B./	21	6856191	B2	2005-02-15	Bartuni	
/E.B./	22	7024608	B2	2006-04-04	Kurokami	

If you wish to add additional U.S. Patent citation information please click the Add button.

U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
/E.B./	1	20020090915	A1	2002-07-11	Komara, et al.	
/E.B./	2	20020101937	A1	2002-08-01	Antonio, et al.	
/E.B./	3	20030016741	A1	2008-04-10	Sasson, et al.	
/E.B./	4	20030058959	A1	2003-03-27	Rafie, et al.	
/E.B./	5	20030174783	A1	2003-09-18	Rahman, et al.	
/E.B./	6	20030223508	A1	2003-12-04	Ding, et al.	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99) Application Number 10766768 Filing Date 2004-01-27 First Named Inventor Ronald Duane McCallister Art Unit Examiner Name Bayard, Emmanuel Attorney Docket Number 2298-010

r		T	,							
/E.B./	7	20040164791	A1	2004-08	3-26	Batruni				
/E.B./	8	20040208242	A1	2004-10)-21	Batruni				
/E.B./	9	20050101269	A1	2005-05	5-12	Dale, et al.				
/E.B./	10	20050123066	A1	2005-06	i-09	Sarca				
If you wisl	h to a	dd additional U.S. Publ	ished Ap	plication	citatio	n information	please click the Ad	d butte	on.	_
				FOREIC	SN PAT	ENT DOCUM	MENTS			
Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²		Kind Code⁴	Publication Date	Name of Patente Applicant of cited Document		Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	Т5
/E.B./	1	1318643	EP		A1	2003-11-06	Kehlenbach, et al.			
If you wisl	h to a	dd additional Foreign P	atent Do	cument	citation	information p	lease click the Add	butto	n	<u> </u>
			NON	I-PATEN	IT LITE	RATURE DO	CUMENTS	-		
Examiner Initials*	Cite No	Include name of the a (book, magazine, jour publisher, city and/or	nal, seri	al, symp	osium,	catalog, etc),	the article (when a date, pages(s), vol	pprop ume-is	riate), title of the item ssue number(s),	T 5
/E.B./	1	CAVERS, "The Effect of Linearization" IEEE Tran	Quadrati	ure Modu Tech, Vo	lator and	d Demodulator 05/97.	Errors on Adaptive D	igital P	redistorters for Amplifier	
/E.B.	/ 2	DING & ZHOU, "Effects of Even-order Nonlinear Terms on Power Amplifier Modeling and Predistortion Linearization" IEEE Transactions on Vehicular Technology, 08/2002.								

(Not for submission under 37 CFR 1.99)

Application Number		10766768		
Filing Date		2004-01-27		
First Named Inventor Rona		ald Duane McCallister		
Art Unit				
Examiner Name	Bay	ard, Emmanuel		
Attorney Docket Number		2298-010		

1			
/E.B./	3	GRASS, BODHISATYA & MAHARATNA, "A Dual-Mode Synchornous/Asynchronous CORDIC Processor" IHP, IM Technologiepark, 2002.	
/E.B./	4	OHLSSON, "Studies on Implementation of Digital Filters With High Throughput and Low Power Consumption" Institute of Technology, Dept of Electrical Engineering, p 61-73, 06/03.	
/E.B./	5	RAICH, QIAN & ZHOU, "Digital Baseband Predistortion of Nonlinear Power Amplifiers Using Orthogonal Polynomials" Proc. ICASSP 2003, p 689-692, Hong Kong China, 04/2003.	
/E.B./	6	GODAVARTI, "Multiple Antennas in Wireless Communications" Array Signal Processing and Channel Capacity" University of Michigan, P 16-52, 2001.	
/E.B.	7	CHOO, "Mapping LMS Adaptive Filter IP Core to Multiplier-Array FPGA Architecture for High Channel-Density VOIP Line Echo Cancellation" CMP United Business Media, 2003.	
/E.B./	8	WIDROW, MCCOOL & BALL, "The Complex LMS Algorithm" Information Systems Laboratory, Naval Undersea Center, Fleet Engineering Dept, 08/19/1974.	
/E.B.	,9	GLENTIS, KOUKOULAS, & KALOUPTSIDIS, "Efficient Algorithms for Volterra System Identification" IEEE Transactions on Signal Processing, Vol 47, No. 11, Nov 1999.	
/E.B./	10	DING, RAICH, & ZHOU, "A Hammerstein Predistortion Linearization Design Based on the Indirect Learning Architecture" in Proc. IEEE Intl. Conf. Acoust., Speech, Signal Processing, vol. 3, Orlando, FL, May 2002, pp. 2689-2692.	
/E.B./	11	RAZZAGHI & CHANG, "A 10-b,1GSample/s ADC using SiGe BiCMOS Technology" Proceedings of the Custom Integrated Circuits Conference (CICC), 433-436 (September 2003) San Jose, CA.	
/E.B./	12	DANESHRAD, "DSP VLSI Engine for Electronic Linearization of Fiber Optic Links" Wireless Integrated Systems Research Group, UCLA, 02/27/2001.	
/E.B./	13	IBANEZ-DIAZ, PANTALEON, SANTAMARIA, FERNANDEZ & MARTINEZ, "Nonlinearity Estimation in Power Amplifiers Based on Subsampled Temporal Data", IEEE 2001.	

(Not for submission under 37 CFR 1.99)

Application Number		10766768			
Filing Date		2004-01-27			
First Named Inventor	Ron	ald Duane McCallister			
Art Unit	-1				
Examiner Name	Bay	ard, Emmanuel			
Attorney Docket Number		2298-010			

	,	Y							
/E.B./	14	EDMUND COERSMEIER & ERNST ZIELINSKI, "Frequency Selective I Adjustments for OFDM Direct Conversion Transmitters", Mar. 2003.	Q Phase and IQ Ampli	tude Imbalance					
/E.B./	15	SANTAMARIA, IBANEZ, LAZARO, PANTALEON & VIELVA, "Modeling Nonlinear Power Amplifiers in OFDM Systems from Subsampled Data: A Comparative Study Using Real Measurements", 2003.							
/E.B.,	16	EUN, POWERS, "A New Volterra Predistortion Based on the Indirect Learning Architecture", 1997 IEEE.							
/E.B./	17	PARK, WOO, RAICH, KENNEY & ZHOU, "Adaptive Predistortion Linearization of RF Power Amplifiers Using Lookup Tables Generated from Subsampled Data", IEEE 2002.							
/E.B	./ 18	MORGAN & MA, "Reducing Measurement Noise Effects in Digital Predistortion of RF Power Amplifiers", Bell Laboratories, IEEE 2003.							
If you wish	n to ac	dd additional non-patent literature document citation information p	lease click the Add b	utton					
		EXAMINER SIGNATURE							
Examiner	Signa	ture /Emmanuel Bayard/	Date Considered	07/22/2008					
*EXAMINI citation if i	*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								
Standard ST 4 Kind of doo	See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.								